IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

oplicant :

Ohtani, et al.

Art Unit :

2811

Serial No.:

08/958,568

Examiner :

Hu, S.

Filed

October 28, 1997

Title

SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING

THE SAME

Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims

66

53

13

0

\$234

Independent

12

12

\$0

ECHNOLOGY CENTER 2800

TOTAL FEE DUE

\$234

A check for \$234 is attached.

Respectfully submitted,

C. Harris Reg. No. 32,030

Fish & Richardson P.C. PTO Customer No. 20985

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Nancy Grant

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RESPONSE

In response to the action mailed June 26, 2001, please amend the application as follows:

In the claims:

Please amend claims 61, 73-76, 81, 83, and 85-87 as

follows:

61. (Amended) A display device comprising:

a semiconductor island comprising silicon provided on an

a source region and a drain region provided in said

semiconductor island;

insulating surface;

10/05/2001 BSAYASI1 00000030 08958568

01 FC:103

234,00 ND

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Nancy Grant

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